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APPLICATION NO.	FII	ING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/025,905 12/26/2001		Куо Но Мооп	8733.541.00	7606		
30827	7590	01/16/2003				
		& ALDRIDGE LI	EXAMINER			
1900 K STRI WASHINGT	,	20006	GEBREMARIAM, SAMUEL A			
				ART UNIT	PAPER NUMBER	
				2811	1.	
				DATE MAILED: 01/16/2003	$\mathcal{Q}$	

Please find below and/or attached an Office communication concerning this application or proceeding.

·				Application No.		Applicant(s)					
				10/025,905		HO MOON, KYO					
`	Offic	Action Summary		Examiner		Art Unit					
				Samuel A Gebrei		2811					
Peri d fo		ING DATE of this commun	ication appe	ears on the cover	sheet with the c	orrespondence add	dress				
THE N - Exten after S - If the - If NO - Failur - Any re	MAILING E sions of time r SIX (6) MONT period for replant period for replant to reply with eaply received b	O STATUTORY PERIOD F DATE OF THIS COMMUNI may be available under the provisions HS from the mailing date of this comm by specified above is less than thirty (3 by is specified above, the maximum string the set or extended period for reply by the Office later than three months a adjustment. See 37 CFR 1.704(b).	CATION. of 37 CFR 1.136 nunication. 0) days, a reply valutory period wi	6(a). In no event, howe within the statutory min Il apply and will expire seause the application to	ver, may a reply be tim imum of thirty (30) days SIX (6) MONTHS from b become ABANDONEI	nely filed s will be considered timely the mailing date of this co D (35 U.S.C. § 133).	mmunication.				
1)⊠	Respons	ive to communication(s) fi	led on <u>14 N</u>	<u>ovember 2002</u> .							
2a) <u></u> ☐			•	s action is non-fi							
3) Since this application is in condition for allowance except for formal matters, prosecution as to the ments is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.  Disposition of Claims											
4) 🖂	Claim(s)	1-15 is/are pending in the	application.								
•	4a) Of the	above claim(s) is/a	re withdraw	n from consider	ation.						
5)🖂	)⊠ Claim(s) is/are allowed.										
6)⊠	6)⊠ Claim(s) <u>1,2,5-9 and 12-15</u> is/are rejected.										
7) 🗌	7)										
8)[	Claim(s)	are subject to restric	ction and/or	election require	ment.						
Applicati	on Paper	s									
	-	ication is objected to by th									
10) 🔲 -		ng(s) filed on is/are:									
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).											
11) ☐ The proposed drawing correction filed on is: a) ☐ approved b) ☐ disapproved by the Examiner.											
	• • •	ed, corrected drawings are re			tion.						
12) 🗌 -	The oath o	or declaration is objected to	by the Exa	aminer.							
•		J.S.C. §§ 119 and 120									
13)	Acknowle	edgment is made of a clain	n for foreign	priority under 3	5 U.S.C. § 119(a	a)-(d) or (f).					
a)	□All b)[	☐ Some * c)☐ None of:									
	1. Ce	rtified copies of the priority	documents	s have been rece	eived.						
	2. Certified copies of the priority documents have been received in Application No										
	3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).										
		ached detailed Office action	on for a list	of the certified c	opies not receive						
14) 🗌 A	cknowled	gment is made of a claim	for domesti	c priority under 3	55 U.S.C. § 119(	e) (to a provisiona	I application).				
a 15)∐ /	)	ranslation of the foreign la Igment is made of a claim	nguage pro for domesti	visional applicat c priority under :	ion has been red 35 U.S.C. §§ 120	ceived. D and/or 121.					
Attachmen											
2) Notic	e of Draftsp	nces Cited (PTO-892) erson's Patent Drawing Review ( osure Statement(s) (PTO-1449) I		4) 5) 6)	Notice of Informal	y (PTO-413) Paper No Patent Application (PT					
J.S. Patent and T			Office Ac	tion Summary		Part o	of Paper No. 6				



#### DETAILED ACTION

### Election/Restrictions

1. Applicant's election without traverse of group I, claims 1-15 drawn to a semiconductor device in Paper No. 5 is acknowledged.

# Claim Rejections - 35 USC § 103

- 2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
  - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 1, 2, 5-9 and 12-15 are rejected under 35 U.S.C. 103(a) as being unpatentable over Sakamoto US patent No. 6,284,558 in view of admitted prior art and in further view of Brandli et al. US patent No. 5,227,012.

Regarding claims 1 and 9, Sakamoto teaches (figs. 4A-4C) a method of fabricating a device, comprising the steps of: providing a thin film transistor; providing an inorganic insulating film (102) over the thin film transistor; providing an organic insulating film over the inorganic insulating film; and dry etching the organic insulating film and the inorganic insulating film using a mixed ratio gas that etches the organic insulating film faster than the inorganic insulating film.

Sakamoto teaches using wet etch to remove the organic film and using dry etching to remove the inorganic insulating film using a mixed ratio gas.



Sakamoto fails to teach using dry etching to remove the organic film where the mixed ratio gas etches the organic insulating film faster than the inorganic insulating film. Furthermore Sakamoto fails to teach a lower electrode of a storage capacitor.

It is conventional and also taught by Brandli (fig. 4) using a combination of dry etchable organic film (100) and inorganic film (102) in the fabrication of thin film structures.

It is conventional and also taught by admitted prior art to form TFT along with capacitor structure.

It would have been obvious to one of ordinary skill in the art at the time the invention was made to incorporate the dry etching process taught by Brandli and the lower electrode structure of admitted prior art in the process of Sakamoto in order to cut the processing step and store charge. Furthermore the combined process of Sakamoto and Brandli inherently etches the organic insulating film faster than the inorganic insulating film since it is known that organic films have a higher etch rate than inorganic films.

The recitation "a method of fabricating an X-ray detecting device" has not been given patentable weight because the recitation occurs in the preamble. A preamble is generally not accorded any patentable weight where it merely recites the purpose of a process or the intended use of a structure, and where the body of the claim does not depend on the preamble for completeness but, instead, the process steps or structural limitations are able to stand alone. See *In re Hirao*, 535 F.2d 67, 190 USPQ 15 (CCPA 1976) and *Kropa v. Robie*, 187 F.2d 150, 152, 88 USPQ 478, 481 (CCPA 1951).



Regarding claims 2 and 10, Sakamoto teaches substantially the entire claimed process of claim 1 above including the etching rate of the organic insulating film is greater than that of the inorganic insulating film (admitted prior art, page 6, lines 10-20).

Regarding claims 5 and 12, Sakamoto teaches substantially the entire claimed process of claim 1 above including patterning the inorganic insulating film and the organic insulating film to provide a storage insulating film and a first protective film; forming a transparent electrode (27) on the first protective film; forming a second protective film (36) on the first protective film; and providing a pixel electrode (5) on the second protective film (fig. 2, admitted prior).

Regarding claims 6 and 13, Sakamoto teaches (figs. 4A-4E) substantially the entire claimed process of claim 1 above including forming a gate electrode on the substrate (105); forming a gate insulating film (107) over the substrate and over the gate electrode; forming a semiconductor layer (109) on the gate insulating film; and forming source (112) and drain electrodes (111) on the semiconductor layer.

Regarding claims 7 and 14, Sakamoto teaches (figs. 4A-4E) substantially the entire claimed process of claim 1 above including the inorganic insulating film is made from any silicon nitride (102).

Regarding claims 8 and 15, Sakamoto teaches (figs. 4A-4E) substantially the entire claimed process of claim 1 above including the organic insulating film is made from BCB (100).



## Allowable Subject Matter

3. Claims 3, 4, 10 and 11 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Prior art of record fails to anticipate or render obvious the process limitation of the mixed ratio gas contains  $SF_6$ ,  $O_2$ ,  $O_2$ +  $CI_2$  and  $CF_4$ .

### Conclusion

4. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. References D-F are cited as being related to gate dielectric comprising silicon nitride.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Samuel Admassu Gebremariam whose telephone number is 703 305 1913. The examiner can normally be reached on Monday-Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tom Thomas can be reached on 703-308-2772. The fax phone numbers for the organization where this application or proceeding is assigned are 703-308-7722 for regular communications and 703-308-7722 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-0956.

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Application/Control Number: 10/025,905

Art Unit: 2811

Samuel Admassu Gebremariam January 13, 2003

Low power

TOM THOMAS
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2800